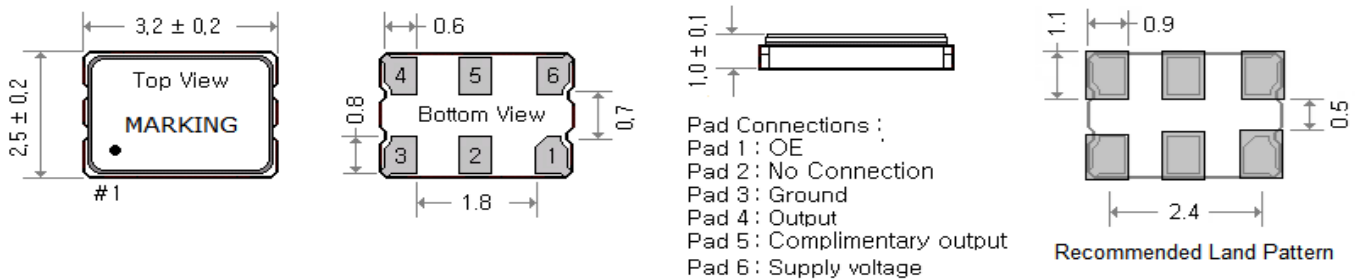


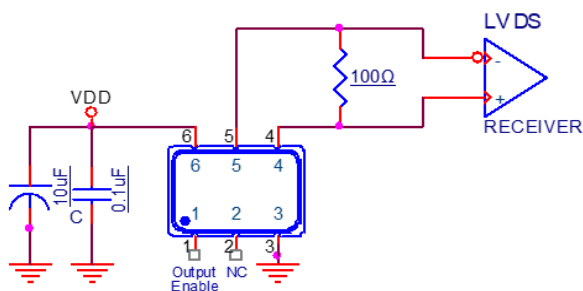
● SPECIFICATION

Model	OSCLD-S3 Series		
Power supply voltage	1.8V DC $\pm 5\%$	2.5V DC $\pm 5\%$	3.3V DC $\pm 5\%$
Output level	LVDS		
Frequency range	13.500 ~ 212.500 MHz		
Storage temperature range	- 55°C ~ + 125°C		
Operation temperature range	- 10°C ~ + 70°C , - 40°C ~ + 85°C , - 40°C ~ + 125°C		
Frequency stability	± 25 ppm ~ ± 100 ppm		
Current consumption	30mA max		
Output Load	100 Ω		
Output voltage level (VoH)	1.60V max.		
Output voltage level (VoL)	0.90V min.		
Differential Output voltage	350mV Typ.		
Start-up Time	10 m sec. max.		
Duty cycle	45% ~ 55%		
Enable input voltage (ViH)	1.260V min.	1.750V min.	2.310V min.
Disable input voltage (ViL)	0.540V max.	0.750V max.	0.990V max.
RISE / FALL TIME (20% / 80%)	40.000~100.000MHz : 1.0nS max.		
	100.000~160.000MHz : 0.5nS max		
Phase Jitter RMS (12KHz~20KHz)	1ps max.		
AGING	± 3 ppm / Year		

● DIMENSIONS (mm)



● TEST CIRCUIT



● OUTPUT WAVEFORM

