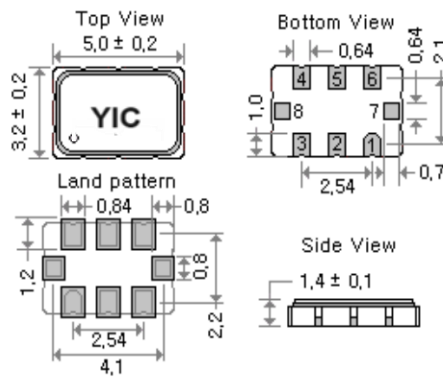


## ● SPECIFICATION

Model	OC -S5 Series		
Power supply voltage	1.8V DC $\pm 5\%$	2.5V DC $\pm 10\%$	3.3V DC $\pm 10\%$
Output level	CML		
Frequency range	15.000 ~ 2100.00 MHz		
Storage temperature range	- 55°C ~ + 125°C		
Operation temperature range	- 20°C ~ + 70°C , - 40°C ~ + 85°C , - 40°C ~ + 125°C , specify		
Frequency stability	$\pm 25$ ppm ~ $\pm 100$ ppm		
Current consumption	90 mA (max) (3.3V) , 80 mA (max) (2.5V) , 70 mA (max) (1.8V)		
Output load	50 $\Omega$ to VDD		
Output voltage level (VOH)	VDD - 0.085V (min) , VDD = (max)		
Output voltage level (VOL)	VDD - 0.6V (min) , VDD - 0.32V ( max)		
Differential output voltage	200 mV (min) , 600 mV (max)		
Start-up time	8 mS (max)		
Duty cycle	45% ~ 55%		
Enable input voltage (VIH)	0.7 x VDD (min)		
Disable input voltage (VIL)	0.3 x VDD (max)		
RISE / FALL TIME (20% / 80%)	0.35 nS (max)		
Phase jitter RMS (12KHz~20KHz)	150 fS (min) , 300 fS (max)		
Aging at Ta = +25°C	$\pm 3$ ppm (max) @ first year		

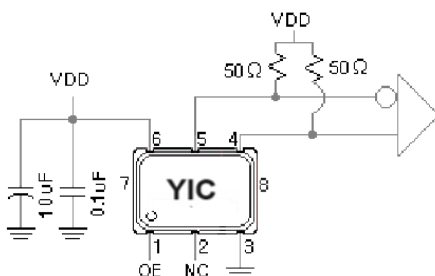
## ● DIMENSIONS (mm)



### PIN CONNECTIONS

- PIN 1 : [ Output Enable ]
- PIN 2 : [ No Connection ]
- PIN 3 : [ Ground ]
- PIN 4 : [ CMOS : Output , Differential : Output ]
- PIN 5 : [ CMOS : No , Differential : Complementary ]
- PIN 6 : [ Supply Voltage ]
- PIN 7 : [ No Connection ]
- PIN 8 : [ No Connection ]

## ● CML Test Circuit



## ● OUTPUT WAVEFORM

